

Call for Papers (ISPS/MIPE 2018)

Conference Website: <https://www.asme.org/events/isps>

Participating Divisions: JSME Information, Intelligence and Precision (IIP) Equipment Division
ASME Information Storage and Processing Systems (ISPS) Division

Conference Chairs: Dr. Aravind Murthy, Western Digital, USA, aravind.murthy@wdc.com

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2018 ASME-ISPS/JSME-IIP Joint International Conference on Micromechatronics for Information and Precision Equipment (MIPE 2018) is now soliciting technical presentations for the conference to be held at Hilton San Francisco Financial District, California, USA, from August 29 to August 30, 2018. Jointly organized by the Information Storage and Processing Systems (ISPS) division of ASME and the Information, Intelligence and Precision Equipment Division (IIP) of JSME, the conference covers a wide spectrum of research topics in areas (listed in the next page). For this year, the MIPE 2018 is co-located with ASME InterPACK Conference (<https://www.asme.org/events/interpack>).

Conference Features

- One Best Paper Award for each technical track.
- One Best Student Paper Award.
- Two ISPS Division Graduate Student Fellowships in the form of USD 2500 each and registration fee waiver.
- Six Conference Scholarships for graduate students in the form of registration fee waiver.
- Discounted registration fees for ASME and JSME members and students, respectively.
- Special MIPE 2018 Issue publication of full papers will be available through Microsystem Technologies Journal (MST)

Important Dates

Submission of 1-page Abstract	March 1, 2018
Author Notification of Abstract Acceptance	March 5, 2018
Submission of 3-page extended abstract for Review	March 26 , April 16, 2018
Author Paper Review Complete/Notification of acceptance	April 2 , May 1, 2018
Electronic Copyright Form Submission Process Opens	April 2 , May 1, 2018
Submission of Copyright Form	April 9 , May 15, 2018
Submission of Final 3-page Paper	May 18, 2018